

FACULTY PROFILE



VENUBABU INALA

Department: INTERNET OF THINGS

College: Gudlavalleru Engineering College

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AddressforCommunication

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AcademicQualifications

S.No.	Nameofthe Degree	University	Percentageof Marks/Grade	Specialization	YearofPass
1	M.Tech	J.N.T.U.K Kakinada	75.6	DECS	2012
2	B.Tech	J.N.T.U. hyderabad	60.27	Electronics and Communication Engineering	2005
3	Intermediate	BIE AP, HYD	88.10	M.P.C	2001
4	S.S.C	BSE AP, HYD	82.33	AP state syllabus	1999

Professional Experience

S.No.	Designation	Institution Name	Working Period	
			From	To
1	Assistant Professor	SR Gudlavalleru Engineering College, Gudlavalleru	20-07-2024	Till date
2	Associate Professor	Sri Vahini Institute of science and Technology, Tiruvuru	06-01-2019	29-06-2019

3	Lecturer	Govt.Polytechinc, vijayawada	02-11-2017	30-12-2018
4	Assistant Professor	Sri Indu Institute of Engineering and Technology, hyderabad	01-06-2015	20-10-2017
5	Assistant Professor	Raghu Engineering college, visakhapatnam	05-06-2014	21-05-2015
6	Assistant Professor	Rise Gandhi group of Institutions, Ongole	02-11-2012	04-06-2014
7	Lecturer/ Asst.Professor	Sri Vishnu Educational Society, Bhimavaram	25-06-2007	18-12-2010

Papers Published in Journals No. : -- 01

- Venubabu inala, manmadharao g “International Journal of Systems, Algorithms & Applications(IJSAA)”, published as special issue,volume 2, August 2012, ISSN online : 2277-2677.

DETAILS OF PATENTS PUBLISHED: 00

ONLINE WORKSHOPS ATTENDED: 00

Papers Published in Conferences No.: 02

- **Venubabu Inala, manmadha rao G** “Performance Evaluation of Stepped frequency LFM signals” published in International Conference on Applied science and engineering (**ICASE-2012**) July 30th, 2012 Taj deccan Hyderabad.
- **Venubabu Inala, manmadha rao G** “ Analysis of stepped frequency pulse Train” published in International Conference on RADAR, Communication and computing(**ICRCC-12**), 21st-22nd December 2012, SKP Engineering College Tiruvannamalai,TN (**IEEE Conference Publications**, record 21904, ISBN 978-1-4673-2758-9).

Workshops Attended No.: 02

- Participated in “A two day National level workshop on VHDL” held in Sri Vishnu Engineering college for Women” Bhimavaram AP.
- Participated in “ A Two Day National level workshop on ES & RTO’s” held in GMRIT Rajam, srikakulam.

Subjects Handled:

Under Graduation

- Electronic Devices and Circuits

- Switching theory and Logic Design
- Digital Electronics
- Analog communication
- Pulse and digital circuits
- Linear Integrated circuits
- Microprocessors and microcontrollers
- Probability Theory and stochastic process
- Random variable and stochastic process
- Electromagnetic theory and transmission lines
- Microwave engineering
- Mobile and cellular communication
- Digital communication
- Radar systems
- Power electronics
- Optical fibre communication

Labs Handled

Under Graduation

- Electronic devices and Circuits
- Linear IC applications
- Analog Circuits
- Microwave and optical Communications
- Microprocessors and microcontrollers
- Analog communication
- Digital communication
- Digital electronics
- Digital signal processing

PostGraduation -- NIL

Other Responsibilities

Department Level

- Held the role of Class Incharge for the various classes of II,III & IV B.Tech

Declaration

I here by declare that all the above-furnished information is correct to the best of my knowledge.

Date: 20-07-2024

Place: Gudlavalleru

Signature

(Venubabu inala)